

FIG. 1

200

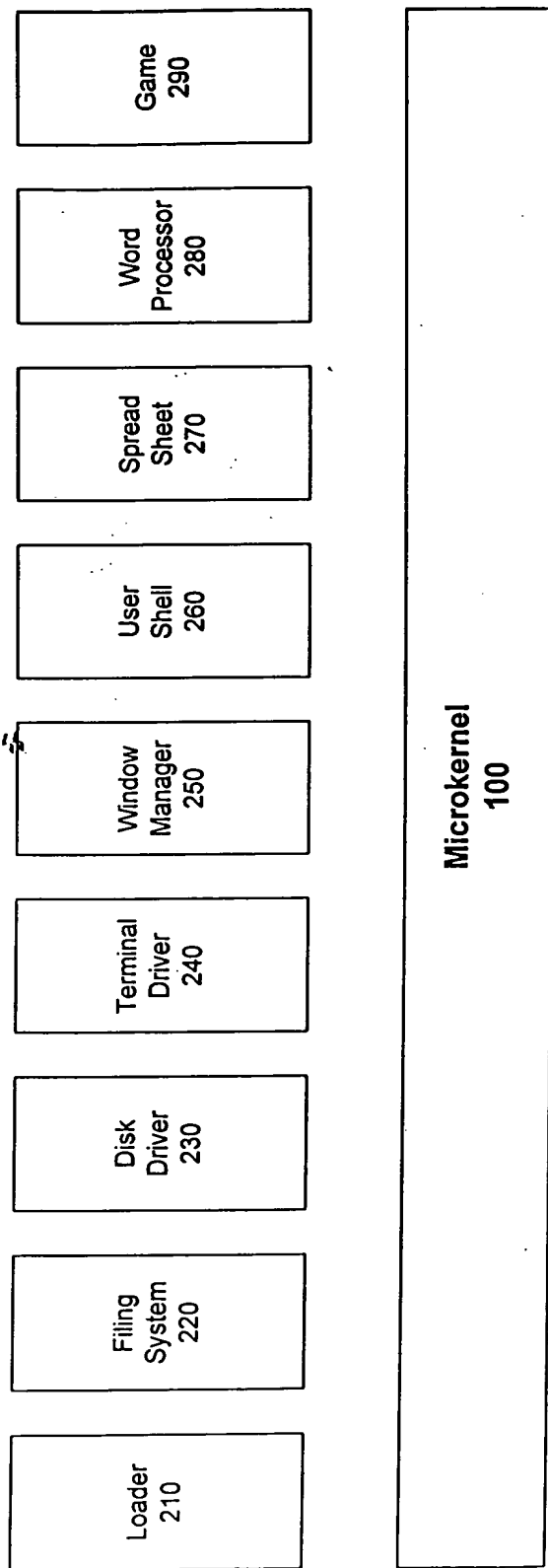


FIG. 2

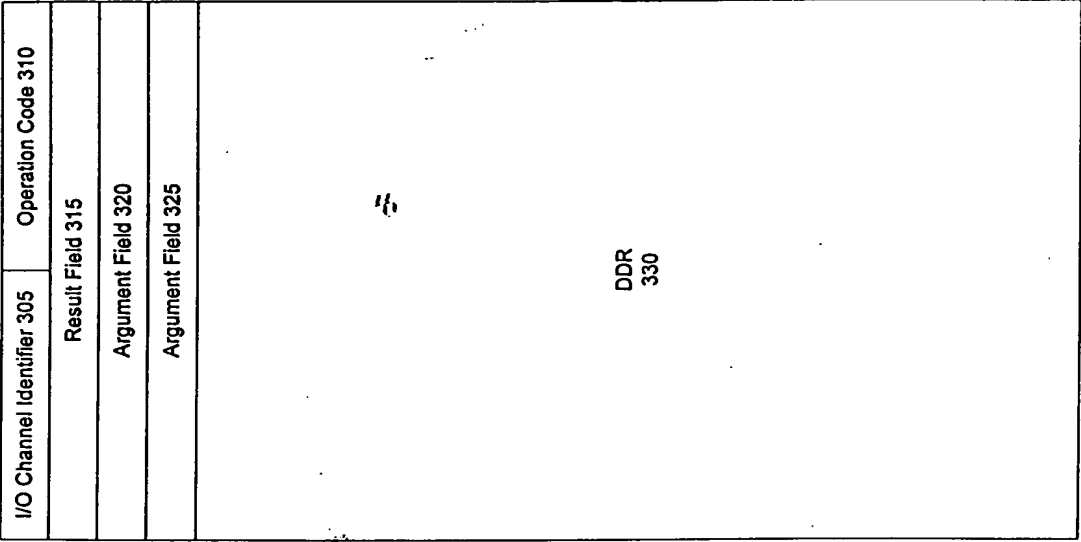


FIG. 3



FIG. 4

DDR
500

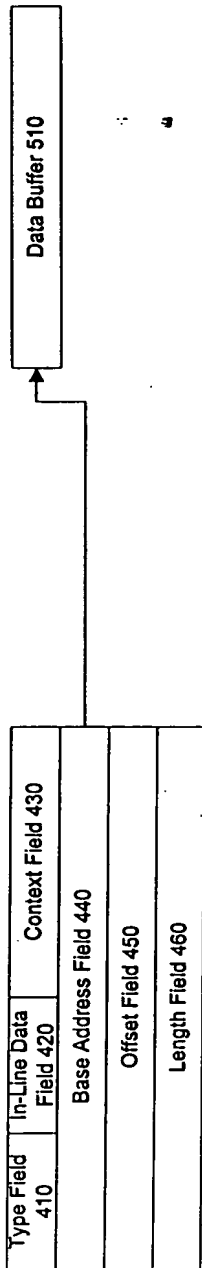


FIG. 5

DDR
600

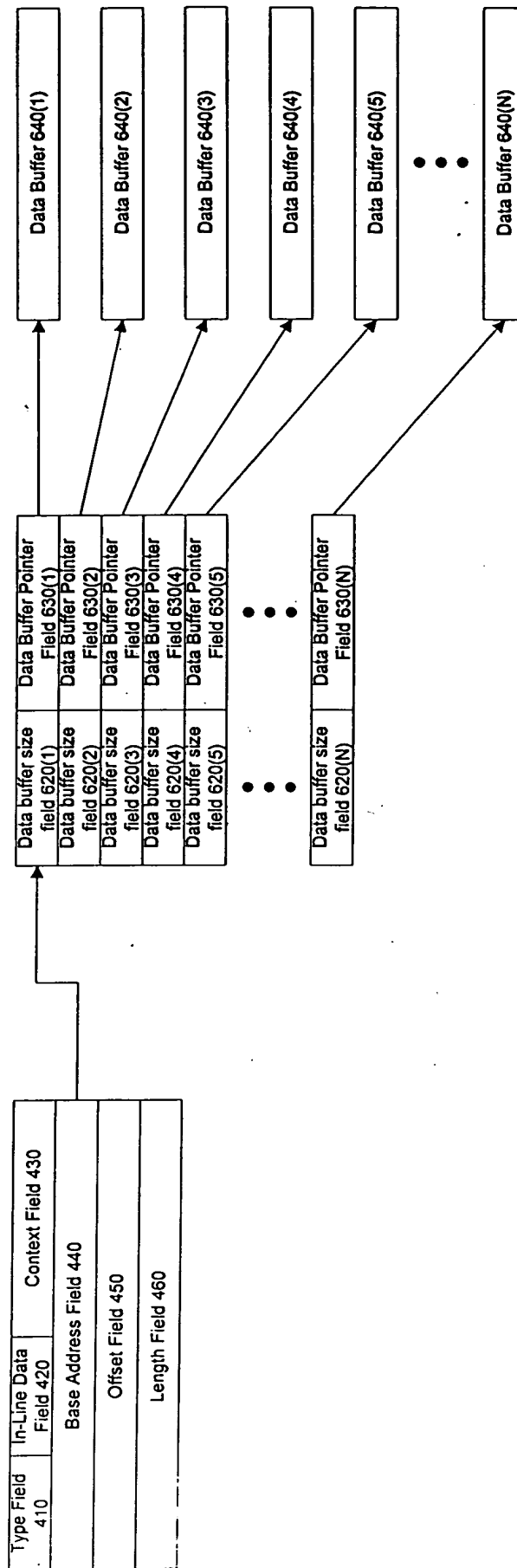


FIG. 6

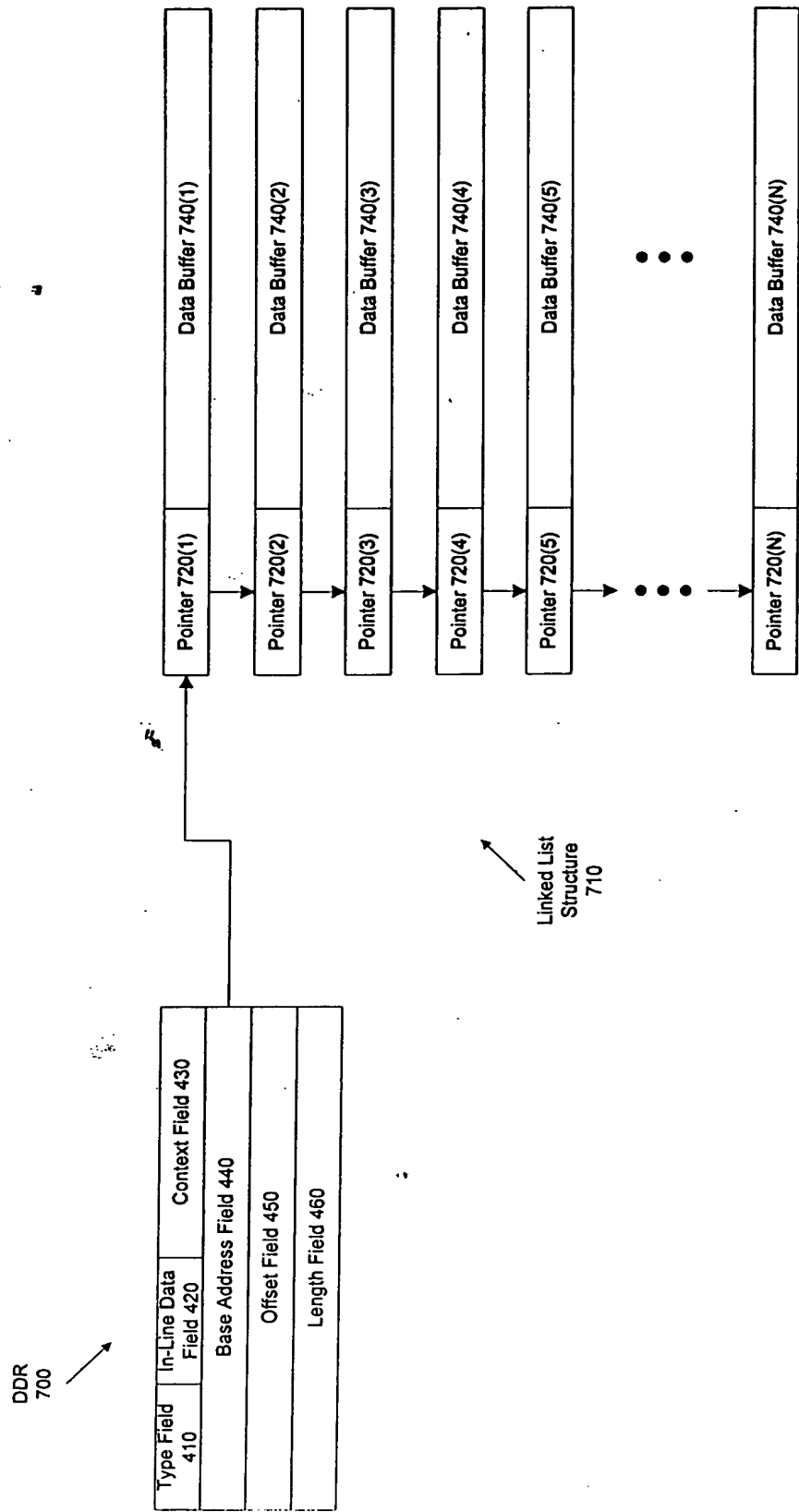


FIG. 7

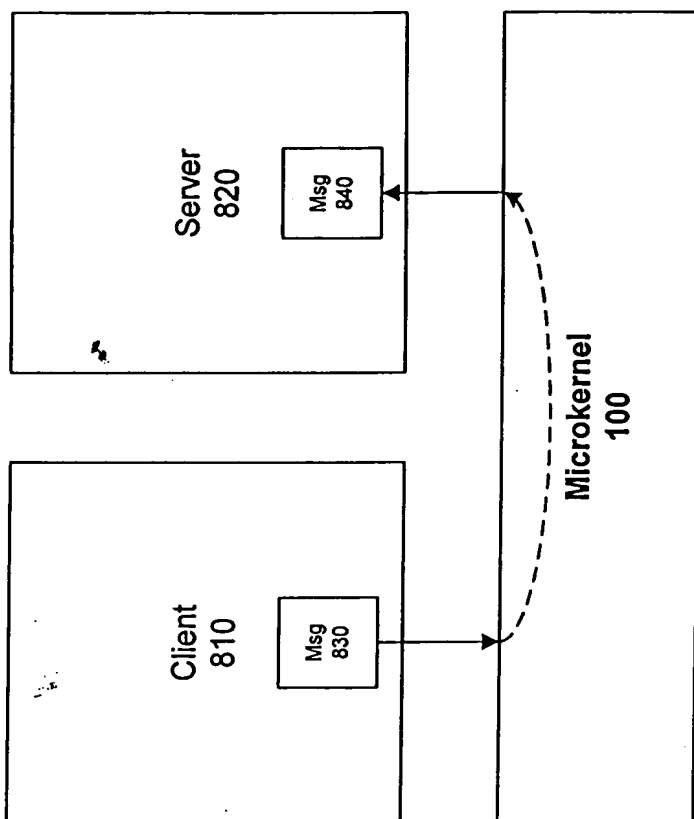


FIG. 8

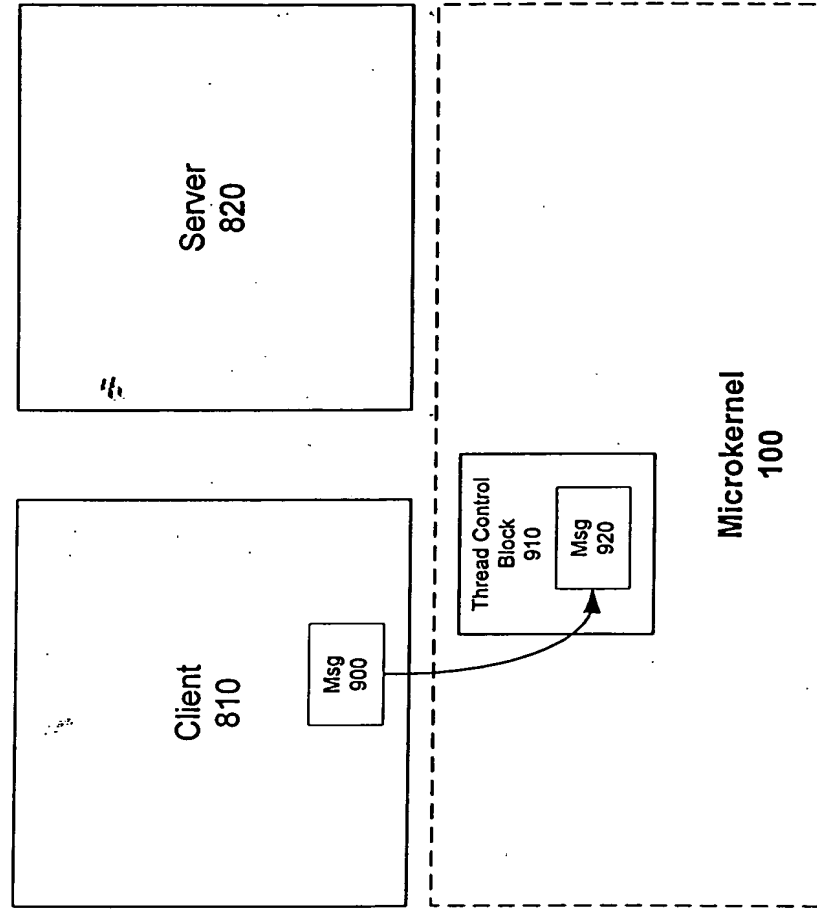


FIG. 9

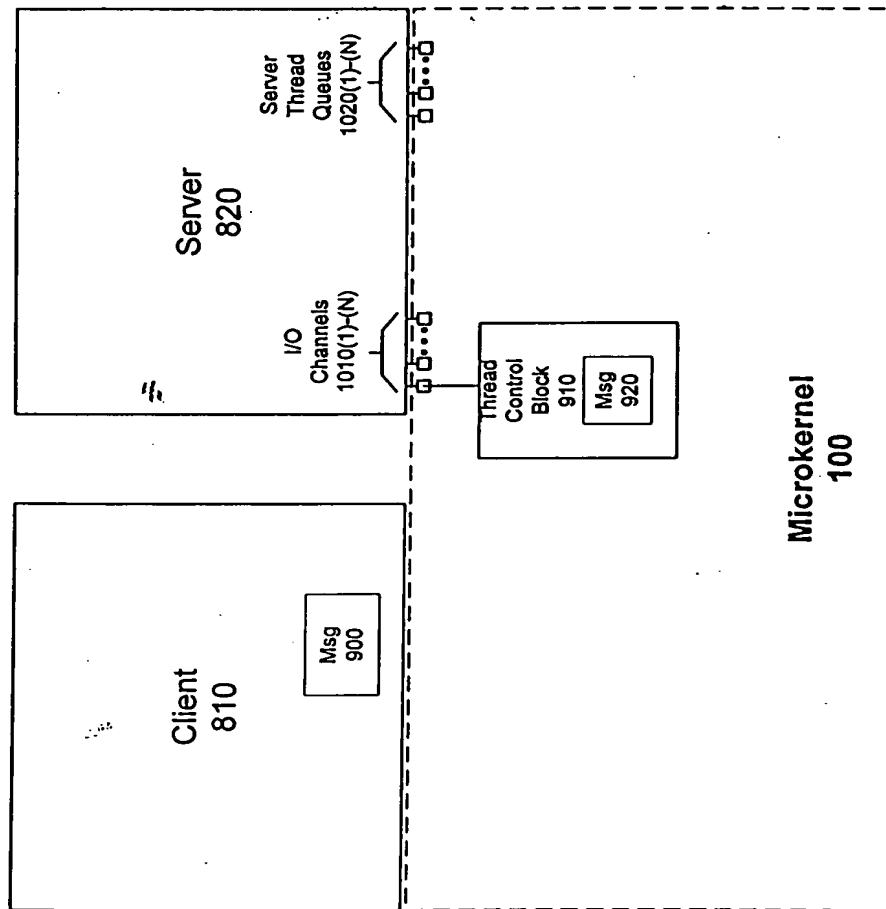


FIG. 10

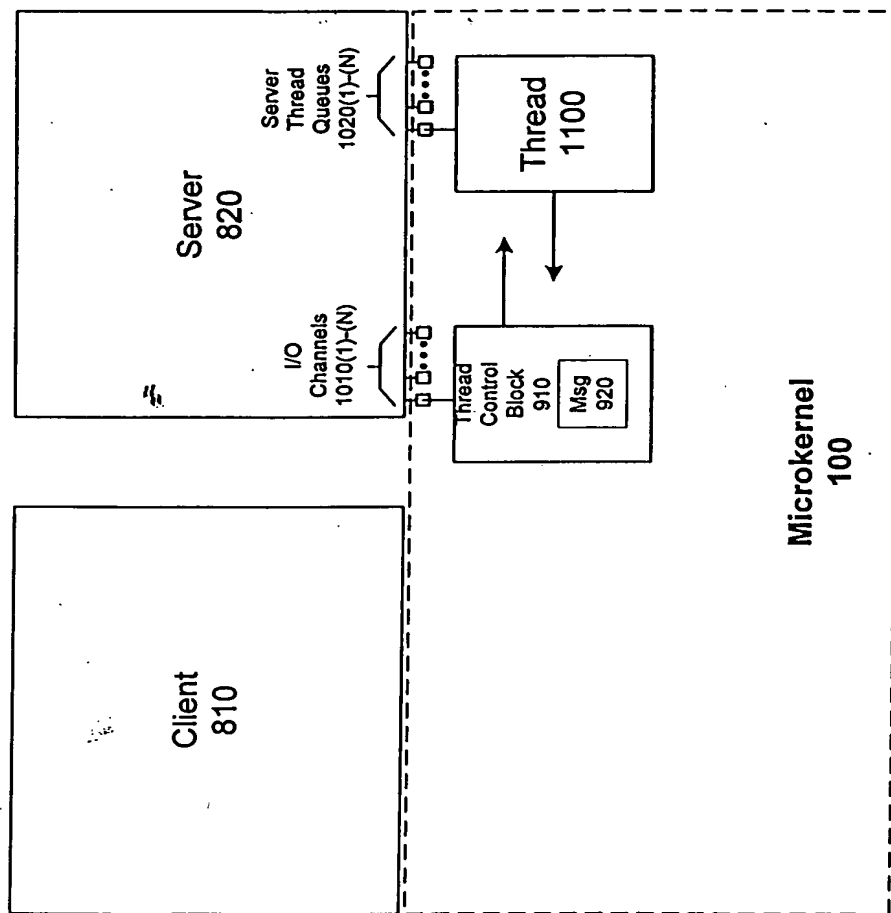


FIG. 11

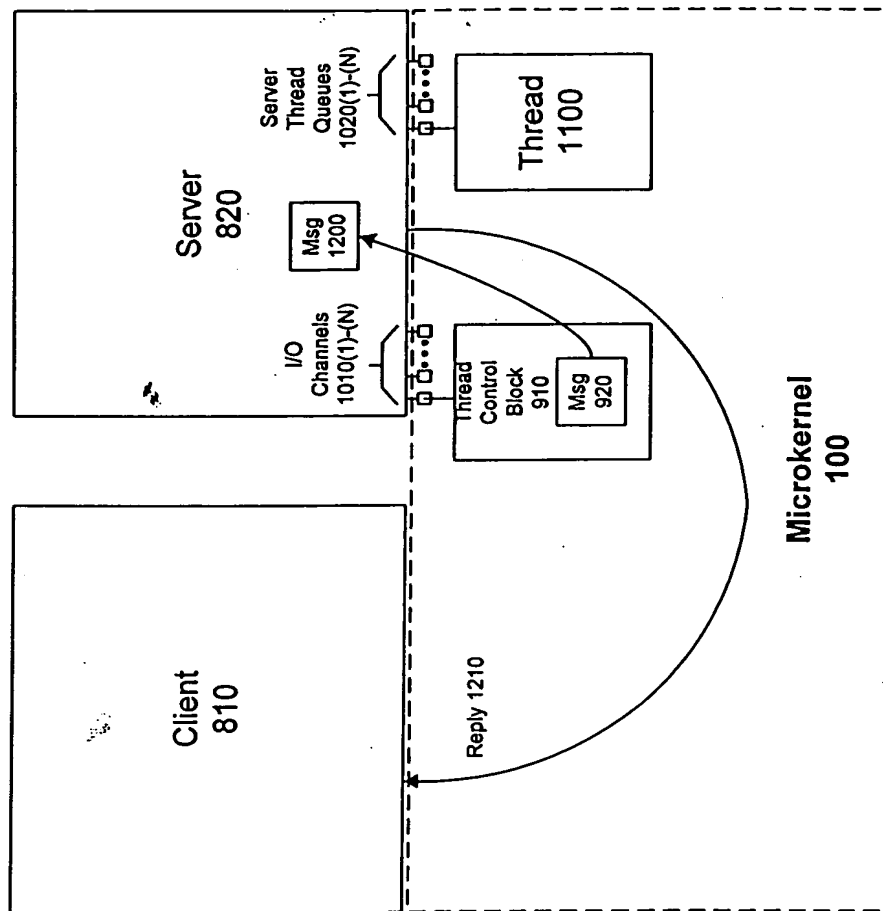


FIG. 12A

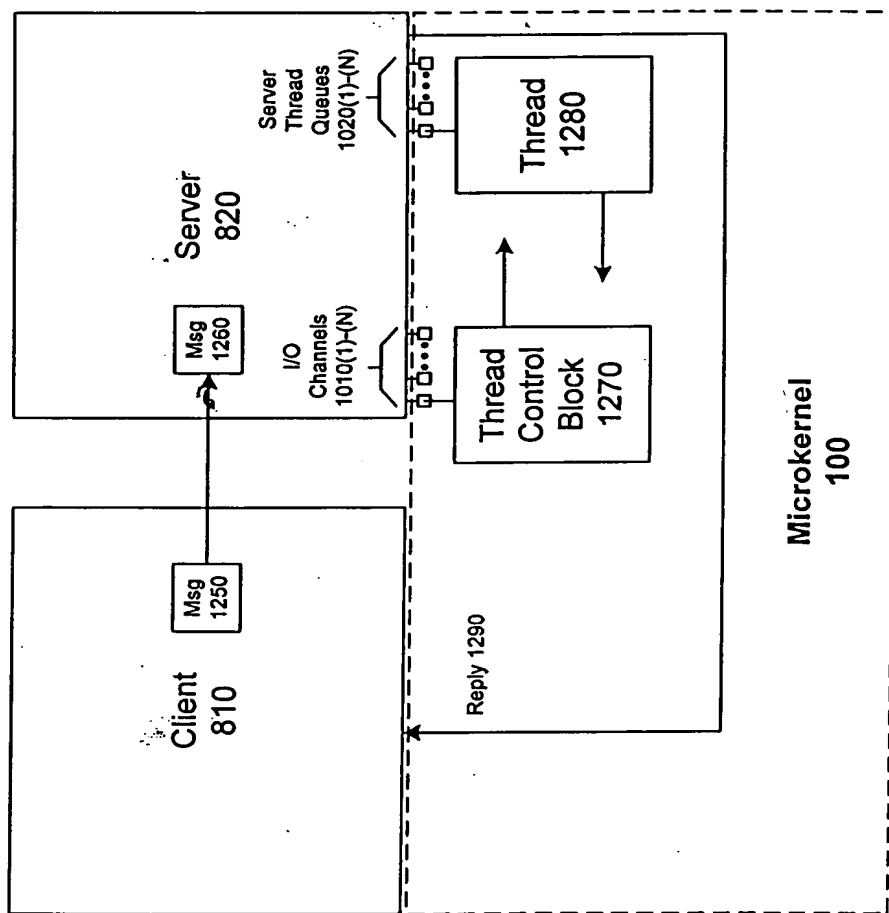


FIG. 12B

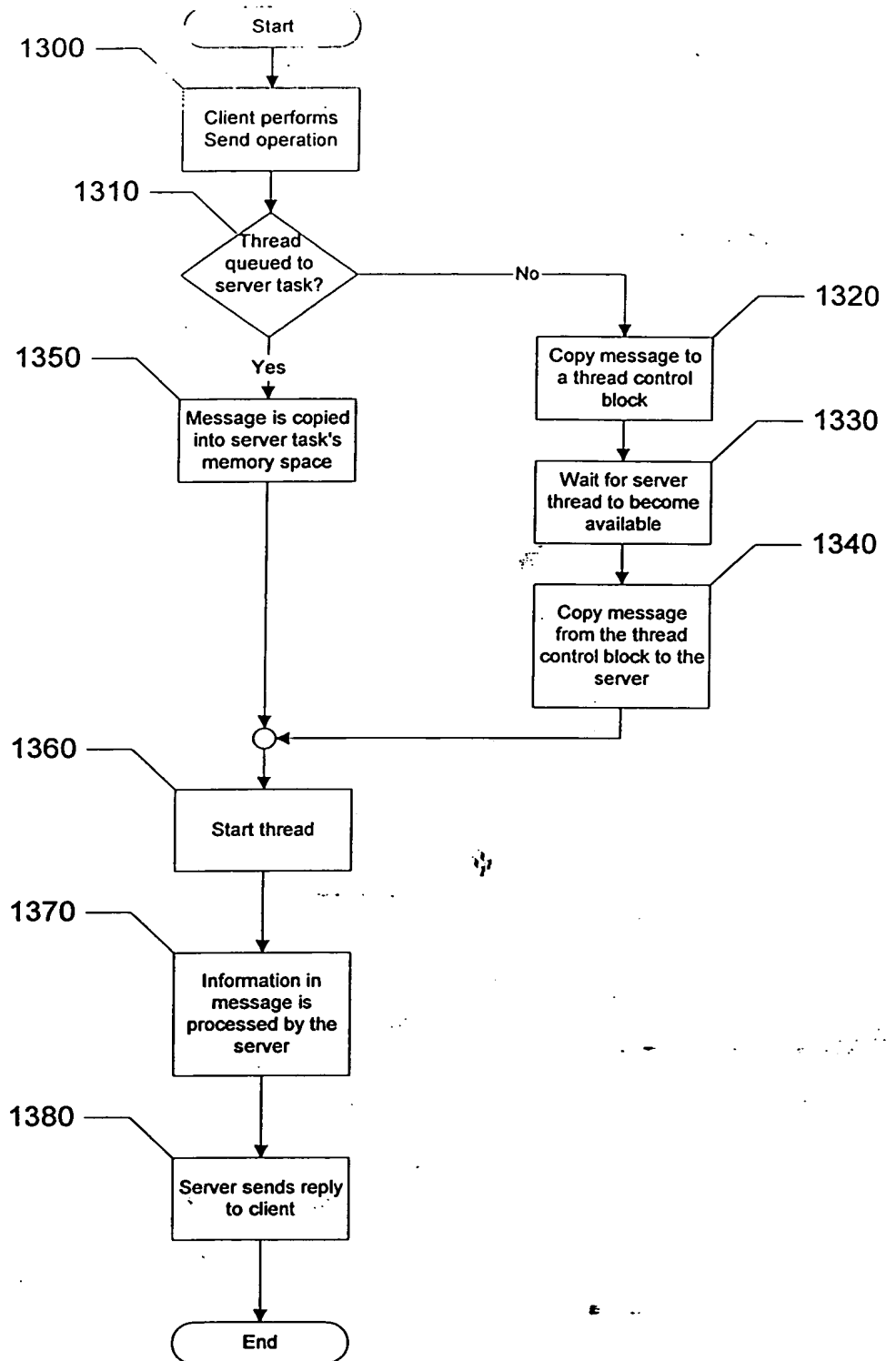


FIG. 13

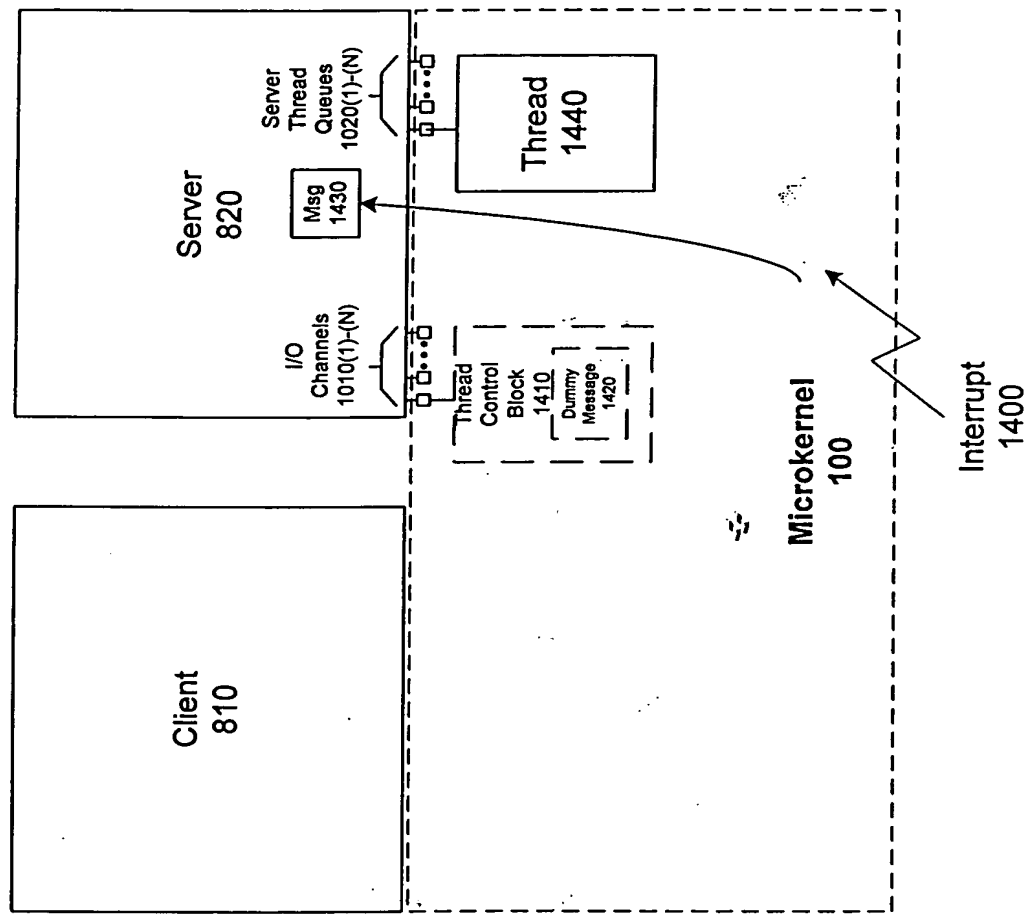


FIG. 14A

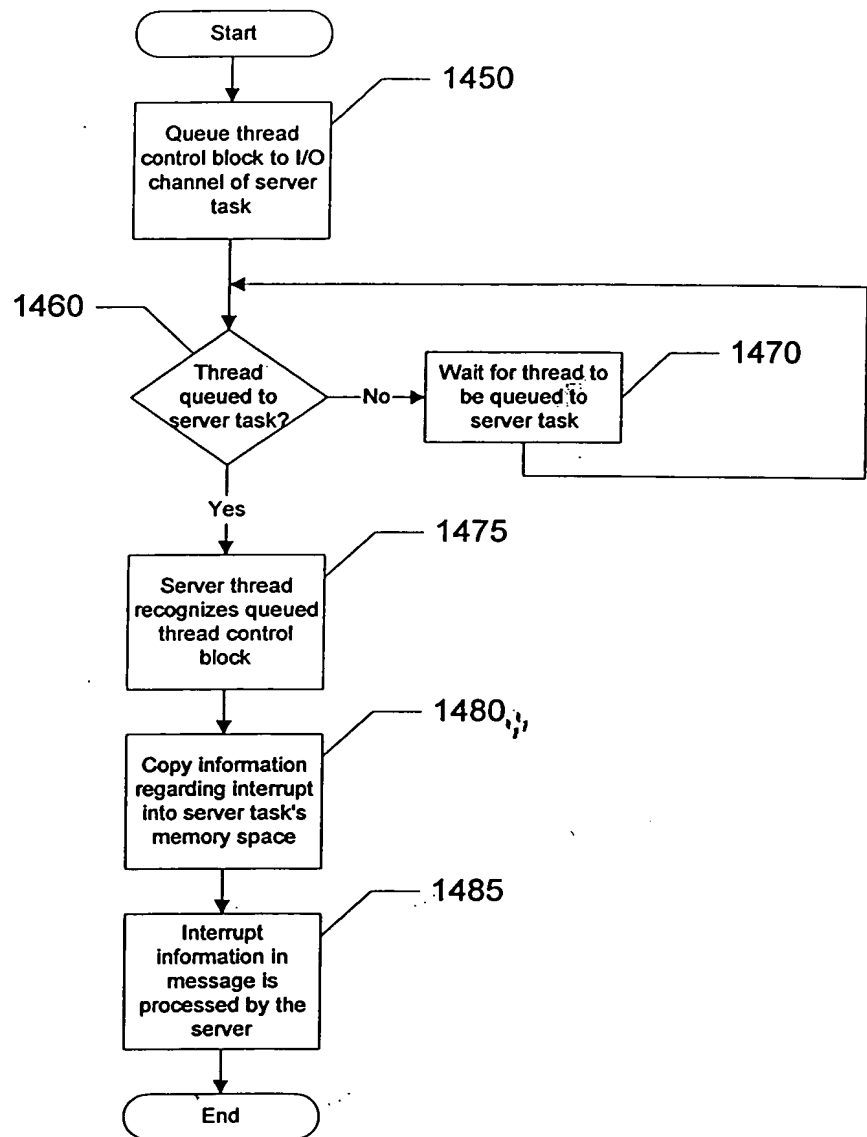


FIG. 14B

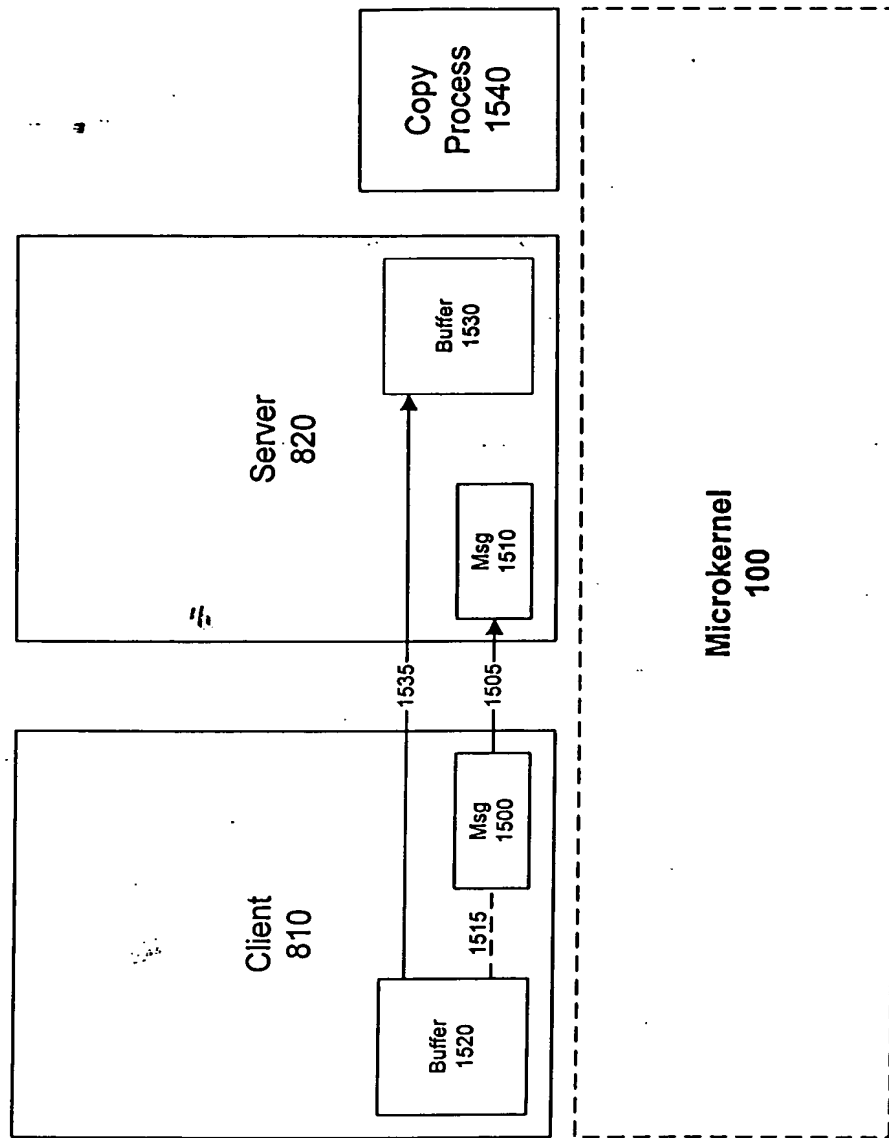


FIG. 15

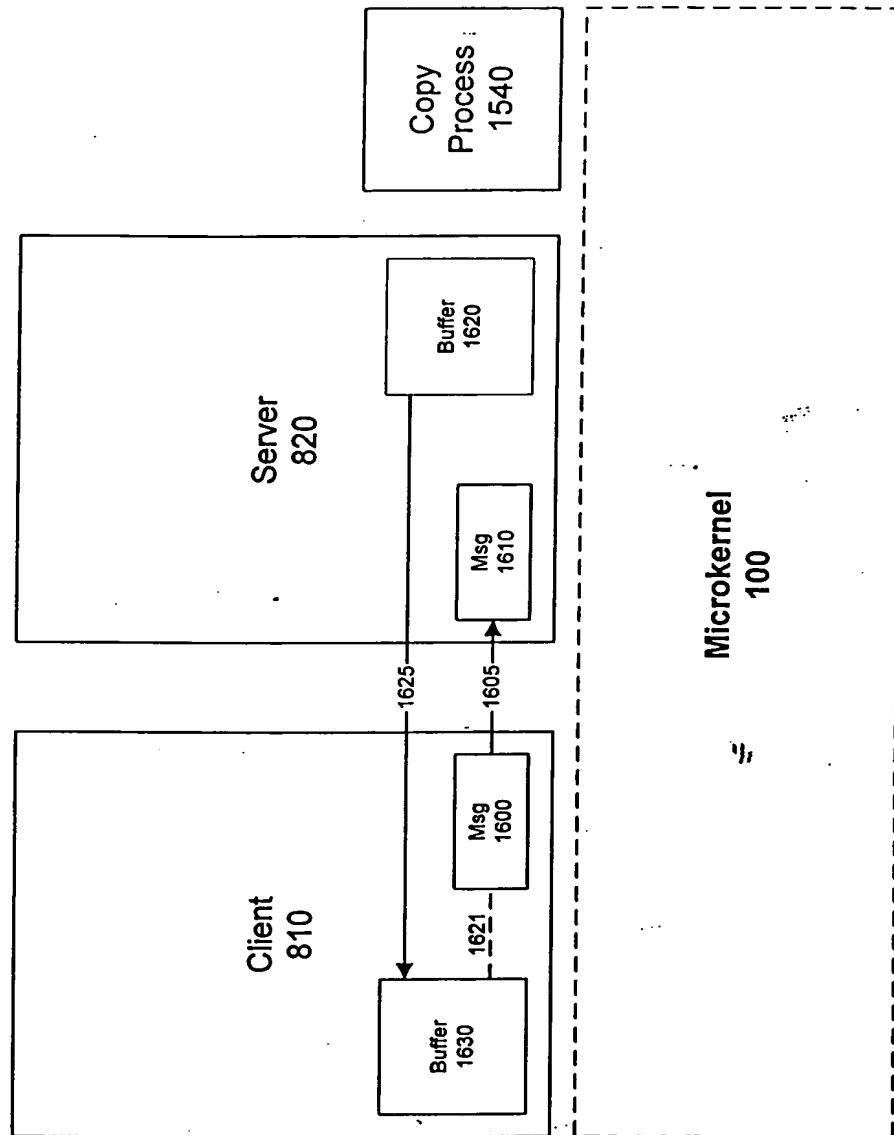


FIG. 16

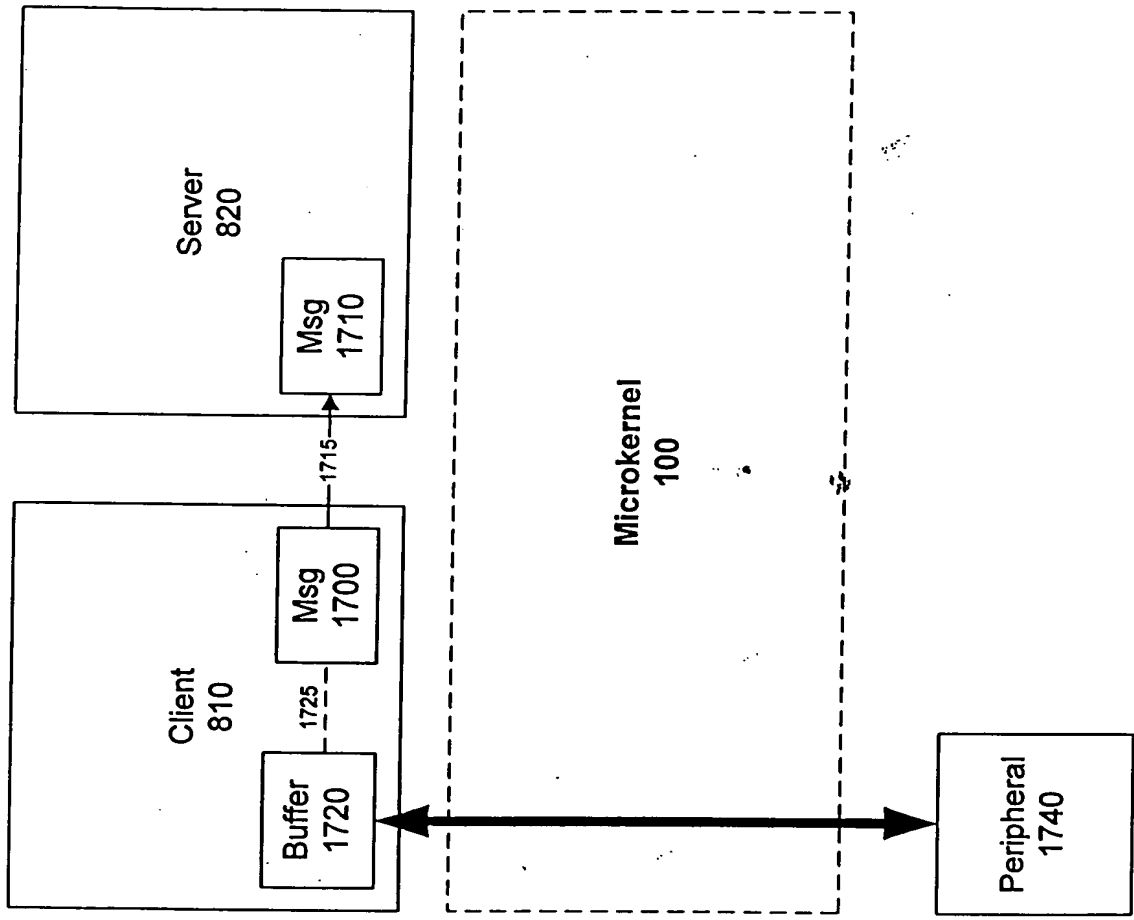


FIG. 17